



**DEPARTMENT OF ELECTRONICS ENGINEERING**  
**SCHOOL OF ENGINEERING AND TECHNOLOGY**  
**PONDICHERRY UNIVERSITY**  
**PONDICHERRY - 605 014**

Ref. No. PU/DEE/RN/Ph.D/MS/2015-16/722

07.04.2016

**Ph.D Synopsis Public Presentation - Notification**

The Public Synopsis presentation of Mrs. M. Shoba, a Full Time Research Scholar in the Department of Electronics Engineering, School of Engineering and Technology, Pondicherry University, Pondicherry will be held as per the schedule detailed below:

---

Title of the Thesis : Design and Implementation of Gate Diffusion Input Logic based Vedic Multiplier

Date and Time : 20.04.2016 (Wednesday) and 03.45 P.M

Venue : Seminar Hall, Department of Electronics Engineering,  
School of Engineering and Technology,  
Pondicherry University, Pondicherry

---

All are cordially invited



*Dr. R. Nakkeeran*

Dr. R. Nakkeeran  
(Supervisor)

**Dr. R. NAKKEERAN**

Head

Department of Electronics Engineering  
School of Engineering & Technology

Pondicherry University

Pondicherry - 605 014

Copy to:

1. AR to the Vice Chancellor, for kind information to the Vice Chancellor, Pondicherry University
2. A.R to the Registrar, for kind information to the Registrar, Pondicherry University
3. The Controller of Examinations, Pondicherry University
4. The Dean, School of Engineering and Technology, Pondicherry University
5. The Dean of all the schools/Heads of all the Departments, Pondicherry University
6. The Members, Doctoral Committee
7. The Librarian, Ananda Ranga Pillai Central Library, Pondicherry University
8. The Deputy Registrar (Academic II), Pondicherry University, for kind information
9. The Heads of the Department of ECE, Engineering Colleges affiliated to Pondicherry University
10. The Systems Manager and Head, Computer Centre, PU-with a request to display in e-circular