

DEPARTMENT OF ELECTRONICS ENGINEERING SCHOOL OF ENGINEERING AND TECHNOLOGY PONDICHERRY UNIVERSITTY **PONDICHERRY-14**

Ph. D Synopsis Public Presentation-Notification

PU/DEE/PS/Ph.D/Synopsis/RV/2016-17/945

Date: 3.11.2016

Name of the Candidate:

Mr. R. Vassoudevan

Title of the Thesis:

Investigations on OTSC based DRX Power Saving

Mechanism for LTE User Equipment

Date and Time:

17th November 2016 (Thursday) and 11.00 A.M

Venue:

Seminar Hall

Department of Electronics Engineering

Pondicherry University

Research supervisor:

Dr. P. Samundiswary

Assistant Professor

Department of Electronics Engineering School of Engineering and Technology

Pondicherry University

Pondicherry-14

Interested Faculty Members, Staff, Research Scholars and Students are cordially invited to attend the synopsis presentation.

Dr. R. Nakkeeran

Head

Dr. P. Samundiswary

Dr. (Mrs.)

Research Supervisor RY, M. Tech., Ph.D. Assistant Professor

Department of Electronics Engineering Copy to: C

Dept. of Electronics Engineering

- 1. A.R to the Vice Chancellor for kind information to the V.C, Pondigher y University

 2. A.R to the Registrar for kind information to the Pondicherty University
- A.R to the Registrar for kind information to the Registrar, Pondicherry University 605 014.
- 3. The Controller of Examinations, Pondicherry University
- 4. The Deputy Registrar (Academic II) for kind information, Pondicherry University
- The Dean, School of Engineering and Technology, Pondicherry University
- 6. The Head, Department of Electronics Engineering, Pondicherry University
- 7. The Members, Doctoral Committee
- 8. The Deans of all Schools/ Heads of all Departments, Pondicherry University
- 9. The Librarian, Ananda Rangapillai Central Library, Pondicherry University
- 10. The System Manager and Head, Computer Centre, PU with a request to host it in e-circular.