REGISTRATION FORM

5 Day National Level Workshop on

ROADMAP TOWARDS 5G WIRELESS INFRASTRUCTURE (R5GW)

Name:
Designation:
Organisation:
Address for Correspondence:
Pin Code:
Phone/ Mobile:
E-mail:
Payment Details:
Amount:
DD No. :
Date:
Name of the Bank & Branch:

Signature

of the applicant

ORGANIZING COMMITTEE

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Convener

Dr. R. Nakkeeran

Head, Dept. of Electronics Engineering

Co-ordinators

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Assistant Professor, DEE

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Member

Dr. T. Shanmuganantham

Assistant Professor, DEE

Contact Information

The Co-ordinators
5 Day National Level Workshop on
RoadMap towards 5G Wireless Infrastructure (R5GW)
Department of Electronics Engineering
School of Engineering and Technology
Pondicherry University, Pondicherry - 605014



DEPARTMENT OF ELECTRONICS ENGINEERING

School of Engineering and Technology Pondicherry University, Pondicherry - 605014

Organises

5 Day National Level Workshop on



ROADMAP TOWARDS 5G WIRELESS INFRASTRUCTURE (R5GW)

2nd to 6th November, 2015

Sponsored by

Phocos India Solar Pvt. Ltd., Pondicherry-605008

Co-Sponsored by

ADML India Pvt. Ltd., Coimbatore-641002

CO-ORDINATORS

Dr. P. Samundiswary & Ms. K. Anusudha

Assistant Professors

Department of Electronics Engineering

Pondicherry University, Pondicherry-605014

OBJECTIVE

5G holds the promise of applications with high social and economic value, leading to hyper connected society in which mobile will play an ever more important role in people's lives. Discussion around 5G falls broadly into two schools of thought. The first vision is hyper connected, which leads to higher coverage & higher network density with the key differentiator as an enabler for Machine 2 Machine (M2M) & the Internet Of Things (IOT). The second vision is Next Generation Radio Technology. This is more of the traditional generation defining view with specific targets for data rates & latency being identified such that new radio interfaces can be assessed against such criteria. This workshop is a forum for researchers and designers to discuss and enhance their expertise in the field of 5G technology with the system model and the protocol stack by considering several issues at different/various layers

ABOUT THE DEPARTMENT

The Department has been started in the year 2010 as the Department of Electronics Engineering under the School of Engineering and Technology. The primary objective of the department is to impart quality education, training and research at Postgraduate and Doctoral level in the front line areas of Electronics and derived Engineering and Technology. The major areas of Faculty expertise of the department includes Antennas, Digital Signal Processing, MEMS, VLSI, Wireless Communications Networks, Optical Communication, Wireless Security and Microwave Wave Devices and Circuits. The department offers two programmes namely, Master of Technology and Doctor of Philosophy in Electronics and Communication Engineering.

ABOUT THE UNIVERSITY

Pondicherry University, established under an act of Parliament in the year 1985. This University has grown from strength to strength in all possible ways all these years and has become a place on the educational hub of the country. 15 Schools, 47 Departments/ Centres offering 153 PG & Research Programs are within its fold and housed in the 800-acres sprawling Wi-Fi enabled vibrant campus. The campus is just 12Kms away from the Puducherry town. It has all the state-of-art facilities in all the schools and departments paving the way for the students to have a student-friendly, result-oriented academic environment with green ambience. The Ananda Rangapillai Library at the University has a collection of more than 2-lakh books and over 24,050 E-journals, 1,41,443 E-books, 41 E-databases and 896 E-theses.

RESOURCE PERSONS

Sessions will be handled by experts from reputed institutions and industries.

VENUE

Seminar Hall, Science and Humanities Block II

IMPORTANT DATES

Last date for receipt of filled in Registration Form: 29-10-2015 Intimation of selection through e-mail: 30-10-2015

REGISTRATION FEE

M.Tech(2nd year students)/ Ph.D Scholars : Rs. 750/~ Faculty Members : Rs. 1250/~ Industry persons : Rs. 1750/~

Interested participants are requested to send the filled-in registration form to the Co-ordinators. Photocopies of the form may be used. Registration fee will be accepted by Demand Draft from INDIAN Bank drawn in the favor of "Co-ordinator - R5GW, Department of Electronics Engineering", payable at Pondicherry along with the completed application form. No TA/DA will be provided. Limited shared accommodations can be made (on request) in the University Guest House (if available) on first come first served basis. (Workshop Kit, Certificate, Working Lunch will be Provided)

DECLARATION

The above information are true to the best of my Knowledge. I agree to abide by the rules and regulations governing the workshop. If selected, I shall attend the workshop for the entire duration.

Date: Signature of the Applicant

SPONSORSHIP CERTIFICATE

Mr/Ms/	Dr.								
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is an employee/student of our Institute and is hereby sponsored to attend the workshop for the entire duration, if selected.

Signature of the Sponsoring Authority

Place:

Office Seal