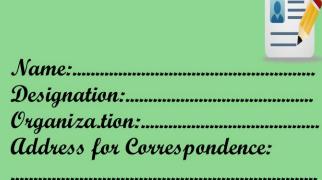
REGISTRATION FORM

National level Workshop on Advances in VLSI Circuit Design using Xilinx

21st and 22nd August, 2014



PIN Code:.....Phone/Mobile:.....

Tayment Details:

 Amount:

 DD No:

Name of the Bank & Branch:

......

Date:

Signature of the Applicant

ORGANIZING COMMITTEE

CHIEF PATRON

Prof. (Mrs.) Chandra Krishnamurthy Vice Chancellor

PATRONS

Dr. V. Indumathi Director (SEI&RR) (i/c)

Dr. S. Balakrishnan Director (Research)

Shri. Raajiv Yaduvanshi IAS Registrar

Shri. K. Vijayakumaran IA &AS Finance Officer

Dr. R. Subramanian
Dean, School of Engineering and Technology

CONVENOR

Dr. R. Nakkeeran Head, Dept. of Electronics Engineering

CO-ORDINATORS

Dr. P. Samundiswary
Assistant Professor, DEE
samundiswary_pdy@yahoo.com
Mobile: 9443268949.
Ms. K. Anusudha

Assistant Professor, DEE anusudhak@yahoo.co.in Mobile: 9940067520.

CONTACT INFORMATION:

The Co-ordinators,

National level Workshop on Advances in VLSI Circuit Design using Xilinx (NWAVCDX)

Department of Electronics Engineering School of Engineering and Technology Pondicherry University, Pondicherry - 605014. National Level Workshop on

Advances in VLSI Circuit Design

using Xilinx

21st and 22nd August, 2014

CO-ORDINATORS

VERS LA LIM

Dr. P. Samundiswary & Ms. K. Anusudha Assistant Professors

ORGANIZED BY

ERARTMENT OF ELECTRONICS ENGINEERING CHOOL OF ENGINEERING AND TECHNOLOGY PONDICHERRY UNIVERSITY PONDICHERRY-605014

OBJECTIVE

Rapid development in the field of VLSI design in recent years is making the life easier and efficient for people by developing compact and high speed chips for various applications. The workshop will serve as a forum for researchers and designers to understand the current topics in VLSI design, electronic design automation and enabling technologies. The objective of the workshop is to make the participants understand the key principles, concepts and techniques involved in VLSI circuit design. The hands on training scheduled in the workshop using XILINX, an industry standard tool, will help the participants to design analog and digital circuits to meet the research challenges.

ABOUT THE DEPARTMENT

The department has been started in the year 2010 as the Department of Electronics Engineering under the School of Engineering and Technology. The primary objective of the department is to impart quality education, training and research at the Postgraduate and Doctoral levels in the front line areas of Electronics and its derived Engineering and Technology. The major areas of faculty expertise of the department include Digital Signal Processing, Antennas, MEMS, VLSI, Wireless Communications and Networks, Optical Communication, Wireless Security and Microwave/Millimeter Wave Devices and Circuits. The department offers two programmes namely, Master of Technology and Doctor of Philosophy in Electronics and Communication Engineering.

ABOUT THE UNIVERSITY

Pondicherry University, established under an Act of Parliament in the year 1985. This University has grown from strength to strength in all possible ways all these years and has become a place on the educational hub of the country. 15 Schools, 50 Departments/Centres offering 153 PG & Research programs are within its fold and housed in the 800-acre sprawling Wi-Fi enabled vibrant campus. The campus is just 12 kms. away from the Puducherry town. It has all the state-of-the-art facilities in all the Schools and Departments paving the way for the students to have a student-friendly, result-oriented academic environment with green ambience. The Ananda Rangapillai Library at the University has a collection of more than 2-lakh books and over 23,858 e-journals, 1,29,404 e-books, 29 e-databases and 773 e-theses.

RESOURCE PERSONS

Sessions will be handled by experts from reputed institutions and industry.

VENUE

Seminar Hall, Department of Electronics Engineering, Science and Humanities Block II, Pondicherry University.

IMPORTANT DATES

Last Date for Receipt of filled in Applications: 8.8.2014
Intimation of Selection through e-mail: 11.8.2014
Confirmation by Participants: 18.8.2014
Date of Workshop: 21.8.2014 & 22.8.2014

REGISTRATION FEE

Rs. 1500/- is the registration fee for all sections of people to participate in the workshop. Interested participants are requested to send the filled-in registration form to the Co-ordinators. Photocopies of the form may be used. Registration fee will be accepted by Demand Draft from INDIAN Bank drawn in favor of "Co-ordinator, NWAVCDX, Department of Electronics Engineering", payable at Pondicherry along with the completed application form. Boarding, lodging and travel expenses have to be borne by the participants. Limited shared accommodations can be made (on request) in the University Guest House (if available) on first come first served basis. Several good hotels are also available in and around Pondicherry. Participants may contact the coordinator(s) on or before 14.8.2014 for accommodation in the University Guest House. No TA/DA will be provided. (Workshop kit, Certificate, Snacks and Lunch will be provided)

DECLARATION

The above information are true to the best of my knowledge. I agree to abide by the rules and regulations governing the workshop. If selected, I shall attend the workshop for the entire duration.

Place :	
Date :	Signature of the Applicant

SPONSORSHIP CERTIFICATE

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	our Institute and is hereby ne workshop for the entire
duration, if selected.	
	Signature
of the	he Sponsoring Authority

Place:	Office Seal
Date :	